



Cmos: Circuit Design, Layout, And Simulation

R Jacob Baker

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In this second edition, the authors explain how to develop design techniques for both long- and short-channel cmos technologies and then compare the two. This approach results in explanations that are multi-dimensional and allows the reader deep insight into the design process. Complete with layout software for the pc, this comprehensive text guides the reader through the process of implementing a chip from the physical definition through the design and simulation of the finished chip. · introduction to cmos design · the well · the metal layers · the active and poly layers · resistors, capacitors, mosfets · mosfet operation · cmos fabrication · electrical noise: an overview · models for analog design · models for digital design · the inverter · static logic gates · clocked circuits · dynamic logic gates · vlsi layout examples · memory circuits · sensing using $\Delta\Sigma$ modulation · special purpose cmos circuits · digital phase-locked loops · current mirrors · amplifiers · differential amplifiers · voltage references · operational amplifiers i · dynamic analog circuits · operational amplifiers ii · nonlinear analog circuits · data converter fundamentals · data converter architectures

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